

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Connie Pin-Chin Wang, et al.

Group Art Unit: 2812

Serial No.:

10/665,938

Examiner: Not yet assigned

Filed: September 17, 2003

Title: SEMICONDUCTOR COMPONENT AND METHOD OF MANUFACTURE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached hereto is a completed Form PTO-1449 listing patents, publications, or other information which the applicant believes may be material to the examination of this application, with copies of each such item enclosed herewith. It is requested that the cited patents be made of record in the examination of this application. This information disclosure statement is being filed prior to the first Office Action and no certification or fee is required. The commissioner is authorized to charge any fee deficiency required by this paper or credit any overpayment to Deposit Account No. 50-2173. A duplicate of this communication is enclosed.

Respectfully submitted,

Dated: 11 February 2004 By

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FORM PTO-1	449 IIS DEP	ARTMENT OF	ATTY. DOCKET NO		<u>.</u>	SERIAL NO
		COMMERCE	H1881			10/665,938
PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT						
			APPLICANT Connie Pin-Chin Wang, et al.			
(Use several sheets if necessary)			FILING DATE September 17, 2003			GROUP 2812
**	,	·	U.S. PATENT DOCUM	ENTS		
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,534,865	03/18/03	Lopatin, et al.			
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		FORE	IGN PATENT DO	CUMENTS		,
DOCUMENT NUMBER		DATE	COUNTRY	CLASS	SUBCLASS	S TRANSLATIO
						YES NO
	OTHER DO	 CUMENTS (I	ncluding Author, T	itle, Date Pert	inent Pages, E	tc.)
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	EXAMINER					